

ABSTRACT

A semiconductor device in which internal data, programs or program instructions can be read when a predetermined data or signal is inputted. It is equipped with protect circuits 31 – 34 that are connected between a CPU 11, and ROM 21, a RAM 22, a register 23 and a user circuit 24, respectively; a debug circuit 12 that, when connected to a debug tool 40, controls the CPU 11 and instructs the protect circuits 31 – 34 not to transfer data, and a protect release circuit 13 that, upon receiving a predetermined signal, instructs the protect circuits 31 – 34 to transfer data without regard to an instruction from the debug circuit 12. The CPU 11 executes predetermined operations when it is not connected to the debug tool 40.